

Notice of References Cited	Application/Control No. 09/998,756	Applicant(s)/Patent Under Reexamination HUNTER ET AL.	
	Examiner J. Derek Rutten	Art Unit 2192	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,011,920	01-2000	Edwards et al.	717/130
	C	US-6,658,578	12-2003	Laurenti et al.	713/324
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

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	V	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach", 1995, Morgan Kaufmann, 2 nd Edition, ISBN 1-55860-329-8, Section 8.3, pp. 654-677
	W	Ziya Aral, Ilya Gertner, "High-level debugging in parasight", November 1988, ACM SIGPLAN Notices , Proceedings of the 1988 ACM SIGPLAN and SIGOPS workshop on Parallel and distributed debugging, Volume 24 Issue 1, ISBN:0-89791-296-9, Pages: 151 - 162
	X	Bannatyne, R., "Debugging aids for systems-on-a-chip", October 1998, Northcon/98 Conference Proceedings, ISBN: 0-7803-5075-8, pp. 159-163

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.